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2813

PATENT  
790001-2004

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants : Hiroyuki YANO et al.  
Serial No. : 09/870,085  
Filed : May 30, 2001  
For : **MANUFACTURING METHOD OF SEMICONDUCTOR  
DEVICE**  
Examiner : Thanhha Pham  
Group Art Unit : 2813

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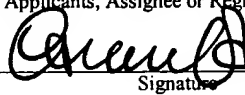
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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, DC 20231, on July 30, 2002.

Grace L. Pan, Reg. No. 39,440

Name of Applicants, Assignee or Registered Representative



Signature

July 30, 2002

Date of Signature

**AMENDMENT**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

This is in response to the Office Action mailed on April 30, 2002, which sets a three-month response period. Please amend this application as follows: